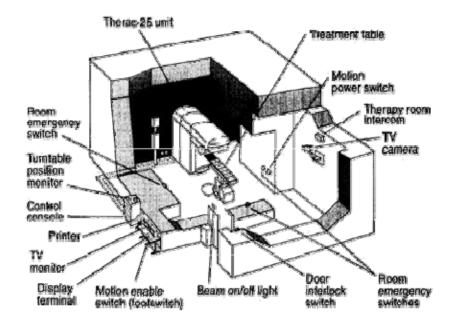
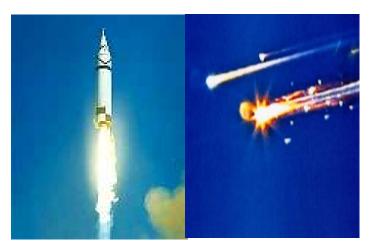
The Spin Model Checker : Part I/II

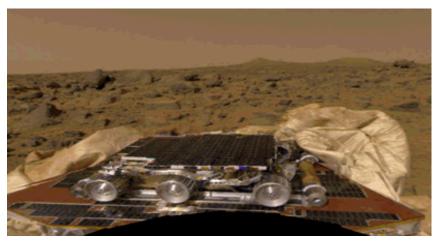
Moonzoo Kim CS Dept. KAIST



Motivation: Tragic Accidents Caused by SW Bugs



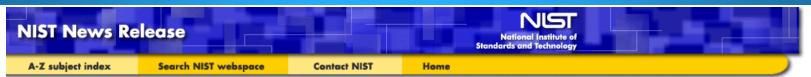








Cost of Software Errors



June 2002

"Software bugs, or errors, are so prevalent and so detrimental that they cost the U.S. economy an estimated \$59.5 billion annually, or about 0.6 percent of the gross domestic product

• • •

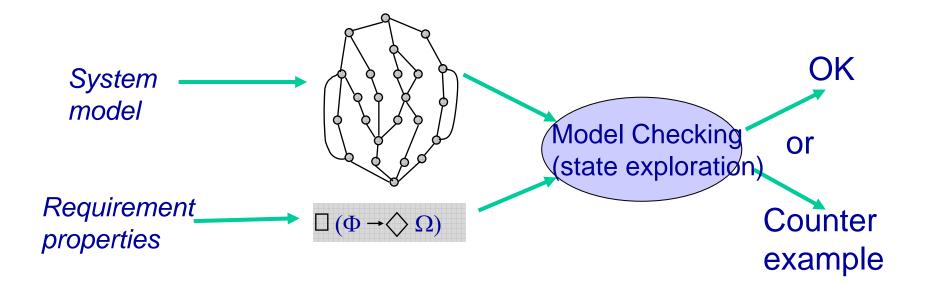
At the national level, over half of the costs are borne by software users and the remainder by software developers/vendors."

The study also found that, although all errors cannot be removed, more than a third of these costs, or an estimated \$22.2 billion, could be eliminated by an improved testing infrastructure that enables earlier and more effective identification and removal of software defects."

NIST Planning Report 02-3 The Economic Impacts of Inadequate Infrastructure for Software Testing



- Specify requirement properties and build system model
- Generate possible states from the model and then check exhaustively whether given requirement properties are satisfied within the state space





- Developed independently by Clarke and Emerson and by Queille and Sifakis in early 1980's.
- Model checking complements testing/simulation.
- Advantages
 - **4** No proofs!!!
 - **4** Fast (compared to other rigorous methods)
 - Diagnostic counterexamples
 - Logics can easily express many concurrency properties

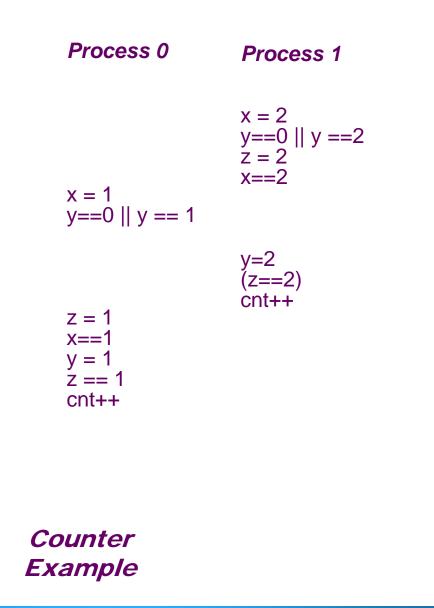


Example. Mutual Exclusion Algorithm

```
char cnt=0,x=0,y=0,z=0;
void process() {
     char me = _pid +1; /* me is 1 or 2*/
again:
     x = me;
     If (y == 0 || y == me);
     else goto again;
     z =me;
     If (x == me);
     else goto again;
     y=me;
     lf(z=me);
     else goto again;
     /* enter critical section */
     cnt++;
     /* assert( cnt ==1); */
     cnt --;
     goto again;
}
                          Mutual
```

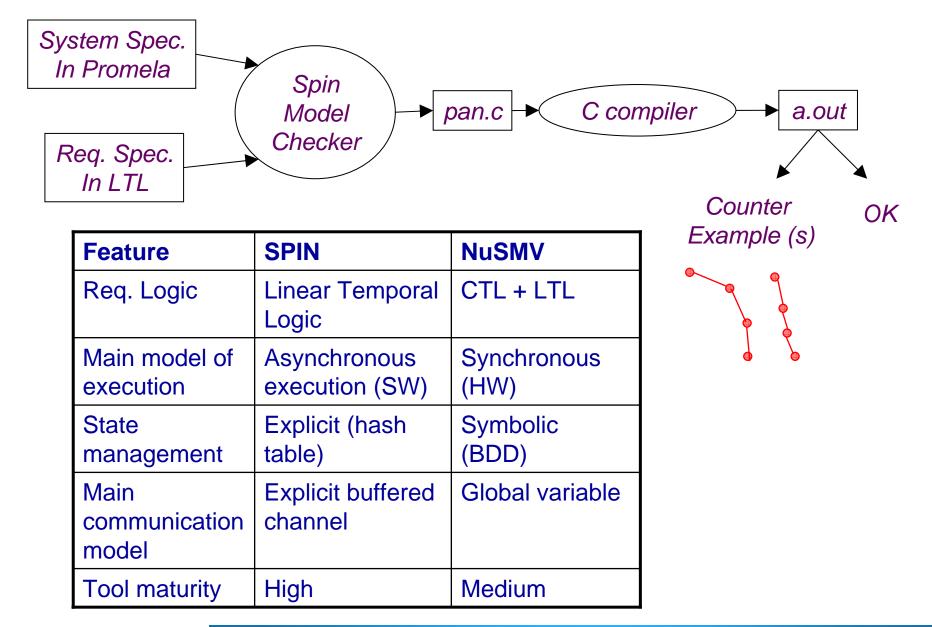
Exclusion

Algorithm





Overview of the Spin Architecture



KAIST CS550 Intro. to SE Spring 2007

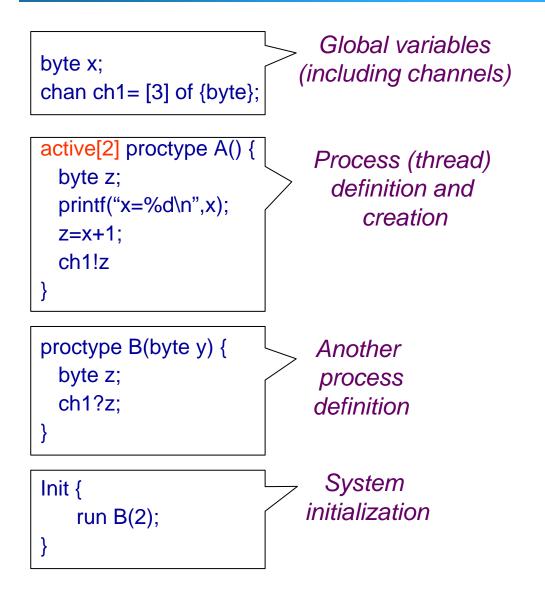
Spin's modeling language - PROMELA

Promela (process meta-language)

- Syntax is similar to that of C, but simplified
 - No float type, no functions, no pointers etc
- Underlying paradigm
 - Communication and concurrency
 - Clear operational semantics
 - Interleaved semantics
 - Asynchronous process execution
 - Two-way communication
- Unique features not found in programming languages
 - Non-determinism (process level and statement level)
 - Executability



Overview of the Promela



Processes are communicating with each other using Global variables Message channels Process can be dynamically created Scheduler executes one process at a time using interleaving semantics



Variables and Types

Basic types

- 4 bit
- \rm 🕹 bool
- Byte (8 bit unsigned integer)
- short (16 bits signed integer)
- Int (32 bits signed integer)
- Arrays
 - **4** bool x[10];
- Records
 - typedef R { bit x; byte y;}
- Default initial value of variables is 0
- Most arithmetic (e.g.,+,-), relational (e.g. >,==) and logical operators of C are supported
 - bitshift operators are supported too.

Promela spec generates only a finite state model because
 Max # of active process <= 255
 Each process has only finite length of codes
 Each variable is of finite datatype
 All message channels have bounded capability <= 255



Basic Statements

Each Promela statement is either

- executable:
- blocked
- There are six types of statement
 - Assignment: always executable
 - Ex. x=3+x, x=run A()
 - Print: always executable
 - Ex.printf("Process %d is created.\n",_pid);
 - Assertion: always executable
 - Ex. assert(x + y == z)
 - Expression: depends on its value
 - Ex. x+3>0, 0, 1, 2
 - Ex. skip, true
 - Send: depends on buffer status
 - Ex. ch1!m is executable only if ch1 is not full
 - Receive: depends on buffer status
 - Ex. ch1?m is executable only if ch1 is not empty



An expression is also a statement It is executable if it evaluates to non-zero 41 : always executable ↓1<2:always executable</p> 4x<0: executable only when x < 0 +x-1:executable only when x !=0If an expression statement in blocked, it remains blocked until other process changes the condition an expression e is equivalent to while(!e); in C



assert(expr)

- 4assert is always executable
- If expr is 0, SPIN detects this violation
- Assert is most frequently used checking method, especially as a form of invariance
 - ex. active proctype inv() { assert(x== 0);}
 - Note that inv() is equivalent to [] (x==0) in LTL with thanks to interleaving semantics



- Promela provides low-level control mechanism, i.e., goto and label as well as if and do
- Note that non-deterministic selection is supported
- else is predefined variable which becomes true if all guards are false; false otherwise

| <pre>proctype A() { byte x; starting: x= x+1; goto starting; }</pre> | <pre>proctype A() { byte x; if :: x <= 0 -> x=x+1 :: x == 0 -> x=1 fi</pre> | <pre>proctype A() { byte x; do :: x <= 0 -> x=x+1; :: x == 0 -> x=1; :: else -> break od</pre> |
|--|--|--|
| | } | 00 } |



Critical Section Example

```
[root@moonzoo spin test]# ls
                                           crit.pml
                                           [root@moonzoo spin test]# spin -a crit.pml
                                           [root@moonzoo spin test]# ls
                                           crit.pml pan.b pan.c pan.h pan.m pan.t
                                           [root@moonzoo spin_test]# gcc pan.c
bool lock:
                                           [root@moonzoo spin test]# a.out
byte cnt;
                                           pan: assertion violated (cnt<=1) (at depth 8)
                                           pan: wrote crit.pml.trail
active[2] proctype P() {
                                           Full statespace search for:
     !lock -> lock=true:
                                                                   - (none specified)
                                                never claim
                                                assertion violations
     cnt=cnt+1:
                                                                     +
                                                acceptance cycles - (not selected)
     printf("%d is in the crt sec!\n",_pid);
                                                invalid end states
     cnt=cnt-1:
                                           State-vector 36 byte, depth reached 16, errors: 1
     lock=false;
                                              119 states, stored
}
                                               47 states, matched
                                              166 transitions (= stored+matched)
active proctype Invariant() {
                                               0 atomic steps
     assert(cnt <= 1);
                                           hash conflicts: 0 (resolved)
}
                                           4.879 memory usage (Mbyte)
                                           [root@moonzoo spin test]# ls
                                           a.out crit.pml crit.pml.trail pan.b pan.c pan.h
                                           pan.m pan.t
```



Critical Section Example (cont.)

```
[root@moonzoo spin test]# spin -t -p crit.pml
Starting P with pid 0
Starting P with pid 1
Starting Invariant with pid 2
 1: proc 1 (P) line 5 "crit.pml" (state 1)
                                               [(!(lock))]
 2:
     proc 0 (P) line 5 "crit.pml" (state 1) [(!(lock))]
 3:
     proc 1 (P) line 5 "crit.pml" (state 2) [lock = 1]
 4:
     proc 1 (P) line 6 "crit.pml" (state 3) [cnt = (cnt+1)]
      1 is in the crt sec!
 5:
     proc 1 (P) line 7 "crit.pml" (state 4)
                                               [printf('%d is in the crt sec!\\n', pid)]
 6:
     proc 0 (P) line 5 "crit.pml" (state 2) [lock = 1]
 7: proc 0 (P) line 6 "crit.pml" (state 3) [cnt = (cnt+1)]
   0 is in the crt sec!
 8: proc 0 (P) line 7 "crit.pml" (state 4) [printf('%d is in the crt sec!\\n',_pid)]
spin: line 13 "crit.pml", Error: assertion violated
spin: text of failed assertion: assert((cnt<=1))
 9: proc 2 (Invariant) line 13 "crit.pml" (state 1) [assert((cnt<=1))]
spin: trail ends after 9 steps
#processes: 3
          lock = 1
          cnt = 2
 9:
     proc 2 (Invariant) line 14 "crit.pml" (state 2) <valid end state>
     proc 1 (P) line 8 "crit.pml" (state 5)
 9:
     proc 0 (P) line 8 "crit.pml" (state 5)
 9:
3 processes created
   Spring 2007
```

```
bool lock;
                                      [root@moonzoo revised]# a.out
byte cnt;
                                      Full statespace search for:
                                           never claim
                                                              - (none specified)
active[2] proctype P() {
                                           assertion violations
                                                                +
     atomic{ !lock -> lock=true;}
                                           acceptance cycles - (not selected)
     cnt=cnt+1;
                                           invalid end states
                                                               +
     printf("%d is in the crt sec!\n",_pid);
     cnt=cnt-1;
                                      State-vector 36 byte, depth reached 14, errors: 0
     lock=false;
                                         62 states, stored
                                          17 states, matched
}
                                         79 transitions (= stored+matched)
                                          0 atomic steps
active proctype Invariant() {
                                      hash conflicts: 0 (resolved)
     assert(cnt <= 1);
                                      4.879 memory usage (Mbyte)
```

Deadlocked Critical Section Example

[[root@moonzoo deadlocked]# a.out pan: invalid end state (at depth 3)

```
(Spin Version 4.2.7 -- 23 June 2006)
byte cnt;
                                       Warning: Search not completed
                                            + Partial Order Reduction
active[2] proctype P() {
     atomic{ !lock -> lock==true;}
                                       Full statespace search for:
     cnt=cnt+1;
                                            never claim
                                                               - (none specified)
     printf("%d is in the crt sec!\n",_pid); assertion violations +
     cnt=cnt-1;
                                            acceptance cycles - (not selected)
     lock=false;
                                            invalid end states
                                                                  +
}
```

```
active proctype Invariant() {
    assert(cnt <= 1);
}</pre>
```

```
State-vector 36 byte, depth reached 4, errors: 1
5 states, stored
0 states, matched
5 transitions (= stored+matched)
2 atomic steps
hash conflicts: 0 (resolved)
```

4.879 memory usage (Mbyte)



bool lock;

[root@moonzoo deadlocked]# spin -t -p deadlocked_crit.pml Starting P with pid 0 Starting P with pid 1 Starting Invariant with pid 2 1: proc 2 (Invariant) line 13 "deadlocked_crit.pml" (state 1) [assert((cnt<=1))]

2: proc 2 terminates

- 3: proc 1 (P) line 5 "deadlocked_crit.pml" (state 1) [(!(lock))]
- 4: proc 0 (P) line 5 "deadlocked_crit.pml" (state 1) [(!(lock))]
- spin: trail ends after 4 steps

#processes: 2

lock = 0

cnt = 0

- 4: proc 1 (P) line 5 "deadlocked_crit.pml" (state 2)
- 4: proc 0 (P) line 5 "deadlocked_crit.pml" (state 2)

3 processes created



Options in XSPIN

Now you have learned all necessary techniques to verify common problems in the SW development

| Advanced Verification Options _ 	 × | | Basic Verification Options 📃 🗖 🗙 | | |
|---|--|----------------------------------|--|---|
| Physical Memory Available (in Mbytes): | 4000 | explain | Compating Dispersion | Search Mode |
| Estimated State Space Size (states x 10^3 Maximum Search Depth (steps): |): 500 | explain explain | Correctness Properties Safety (state properties) Assertions | Exhaustive Supertrace/Bitstate |
| Nr of hash-functions in Bitstate mode: Extra Compile-Time Directives (Optional): | 2 | explain | Invalid Endstates Liveness (cycles/sequences) | A Full Queue |
| Extra Comple-Time Directives (Optional): | | Choose | Non-Progress Cycles Acceptance Cycles USA: Use: Estimate | Blocks New Msgs Loses New Msgs |
| Extra Verifier Generation Options: | | Choose | With Weak Faimess Apply Never Claim (If Present) | [Add Never Claim from File] |
| Error Trapping | Type of Run | | Apply Never Claim (If Present) Report Unreachable Code | [Verify an LTL Property] |
| Stop at Error Nr: 1 Don't Stop at Errors | Use Partial Order Redu | ction | Check xr/xs Assertions | [Set Advanced Options] Help Cancel Run |
| Save All Error-trails Find Shortest Trail (iterative) | Add Complexity Profilin Compute Variable Rang | - | | |
| Use Breadth-First Search | | | | |
| Help | Cano | el <mark>Set</mark> | | |



Spin home page

- <u>http://www.spinroot.com</u>
 - Tool downloads and documents (tutorials, online reference, etc)
- The Spin Model Checker by G.Holzmann – 2nd ed, Addison Wesley

